

Implementing and Evaluating Successor Generators in the Fast Downward Planning System Bachelor Thesis

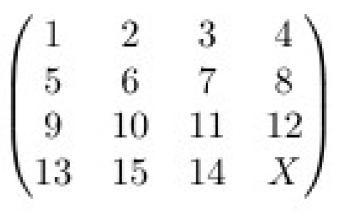
Yannick Zutter, 09.10.2020

Agenda.

| 1 | Introduction – What is Planning |
|---|---|
| 2 | The Successor Generators – Naive |
| 3 | The Successor Generators – Fast Downward |
| 4 | The Successor Generators – Marking |
| 5 | The Successor Generators – PSVN |
| 6 | The Successor Generators – Watched Literals |
| 7 | Evaluation |

Find sequence of operators to solve a given planning problem

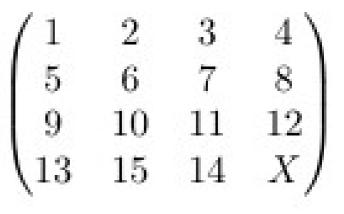




Find sequence of operators to solve a given planning problem

Initial state

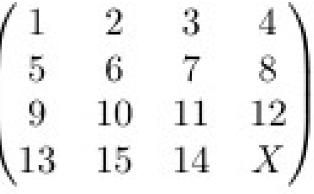




Find sequence of operators to solve a given planning problem

Initial state \rightarrow operator

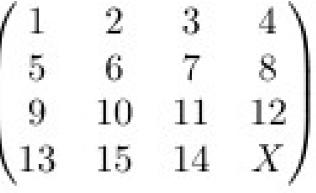




Find sequence of operators to solve a given planning problem

Initial state \rightarrow operator \rightarrow successor state \rightarrow ...





Find sequence of operators to solve a given planning problem

Initial state \rightarrow operator \rightarrow successor state \rightarrow ... \rightarrow goal



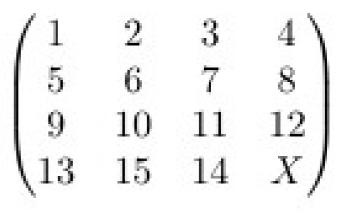
$$\begin{pmatrix} 1 & 2 & 3 & 4 \\ 5 & 6 & 7 & 8 \\ 9 & 10 & 11 & 12 \\ 13 & 15 & 14 & X \end{pmatrix}$$

FDR Planning Task

 $\Pi = (V, s_0, s^*, O)$

- V: set of state variables with finite domain
- **s**_o: initial state as a set over V
- s*: set of goals as partial states
- **O:** set of operators with:
 - pre(o): preconditions as set a of facts
 - eff(o): effect of the operator
 - cost(o): cost





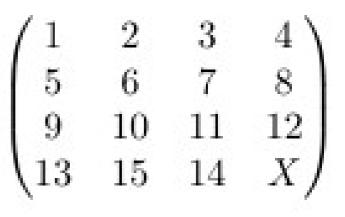
Operators:

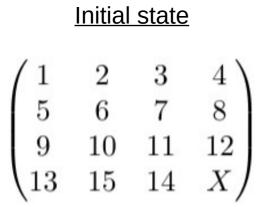
- move_left
- move_right
- move_up
- move_down

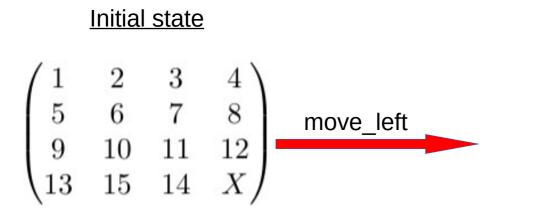
move_left:

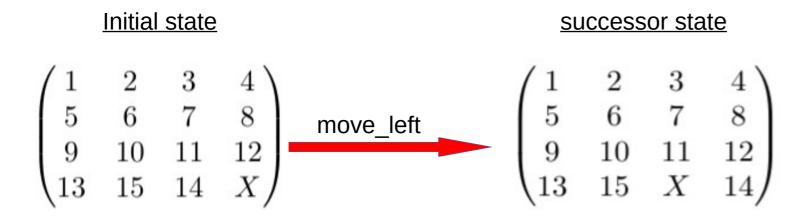
- Precondition: not in outer left column
- Effect: switch X with tile on left
- Cost: 1

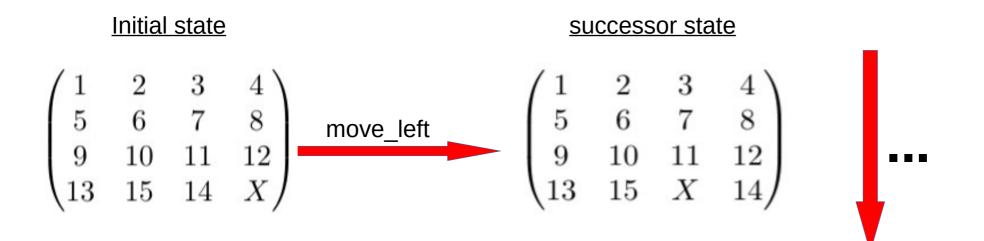


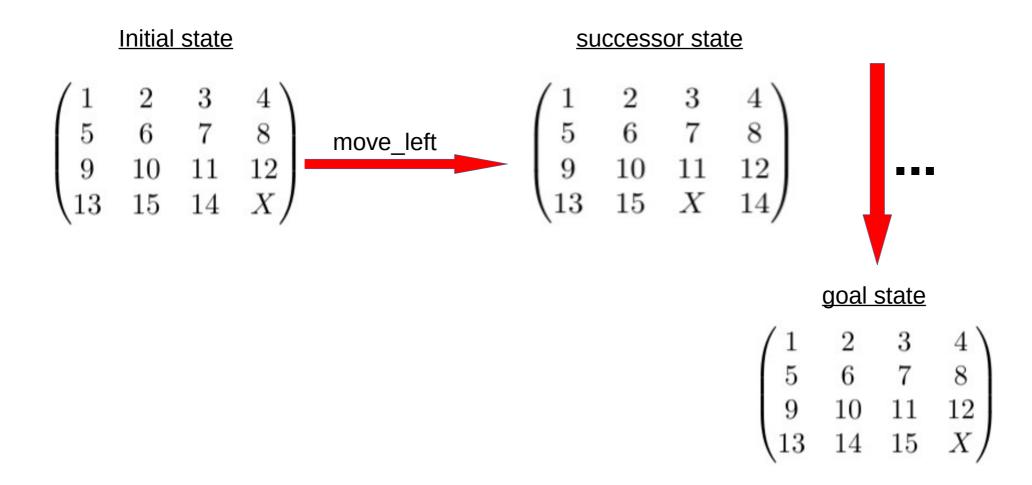






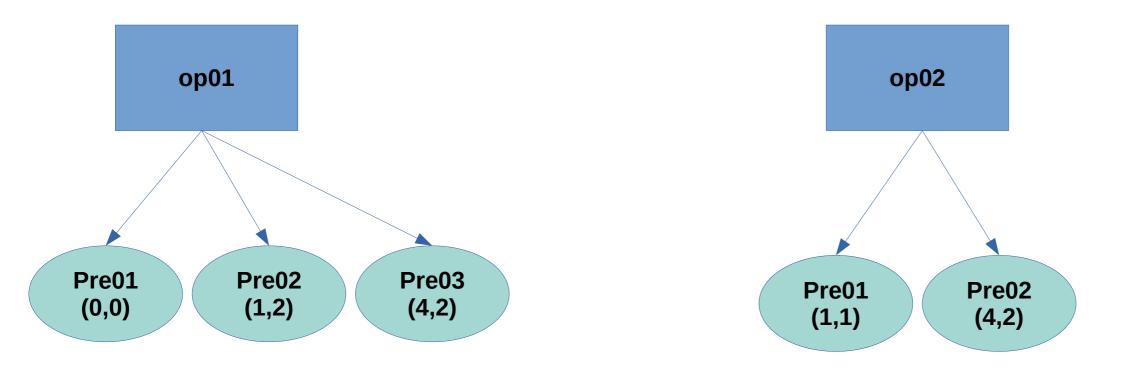


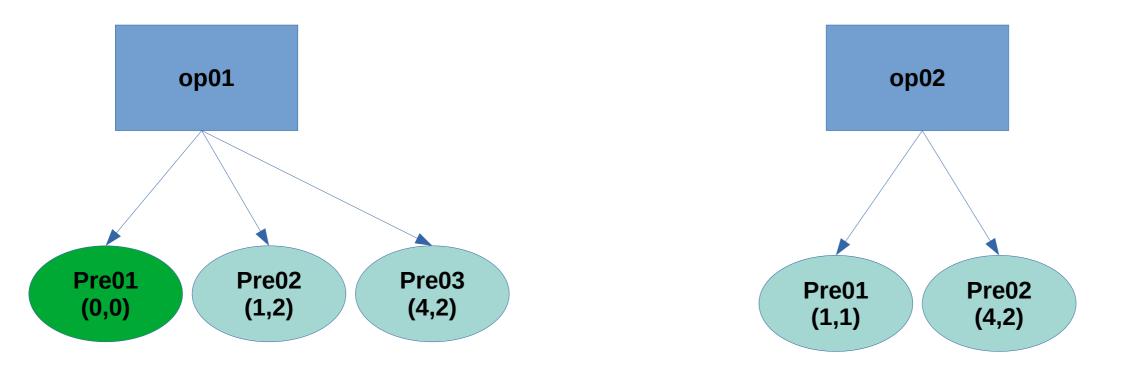


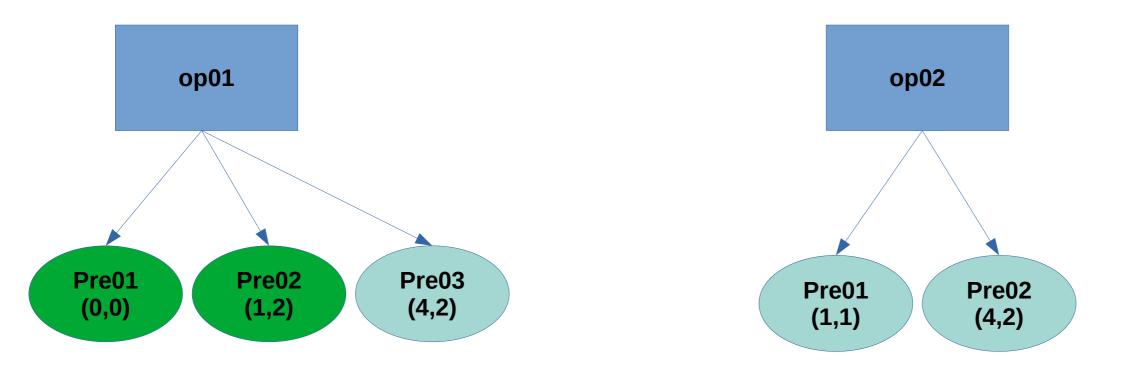


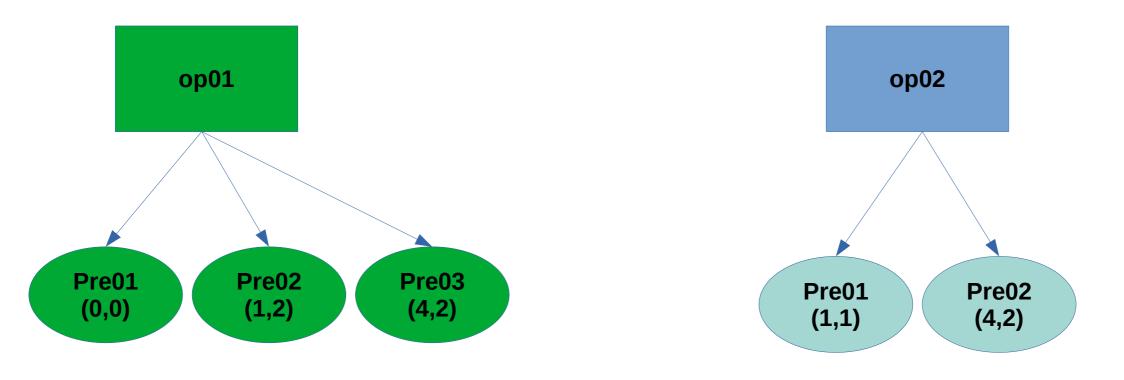
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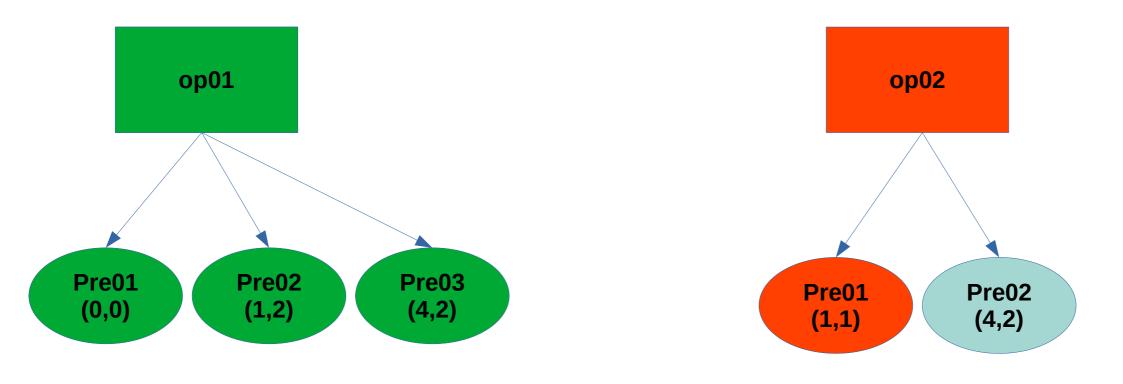
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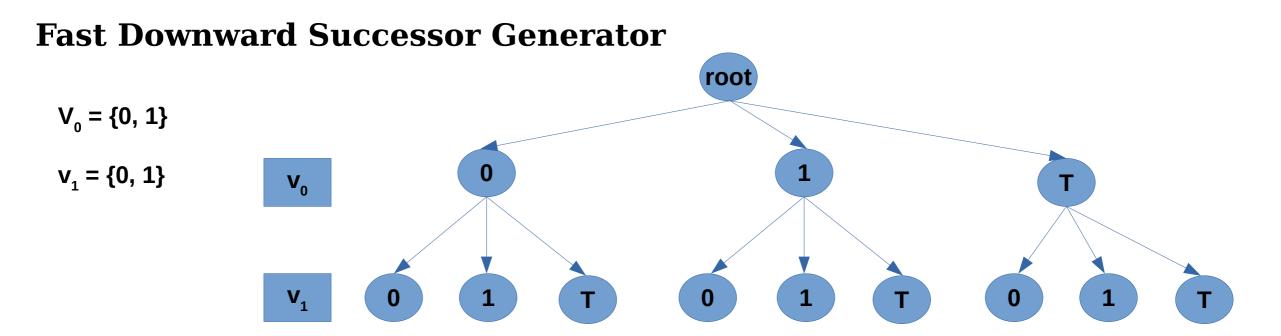


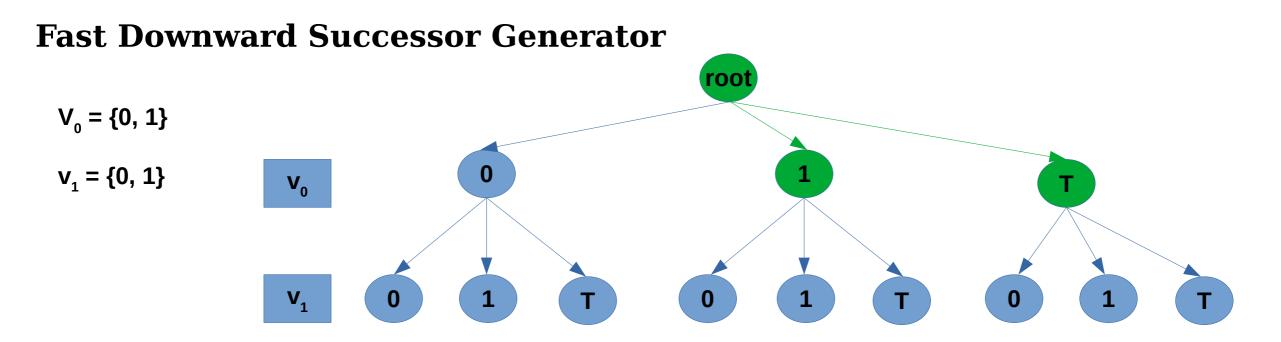




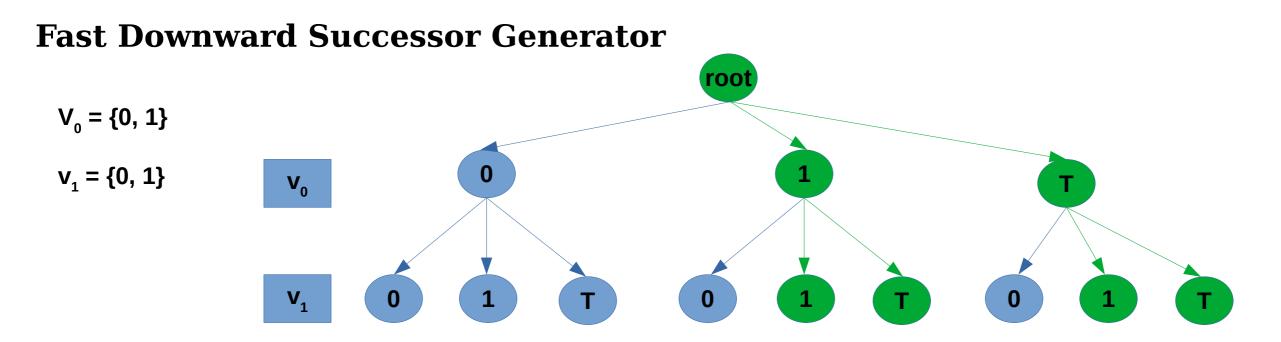
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State: (1,1)

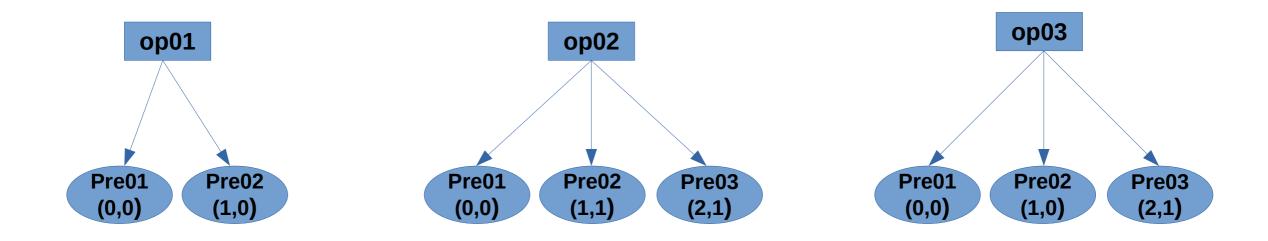


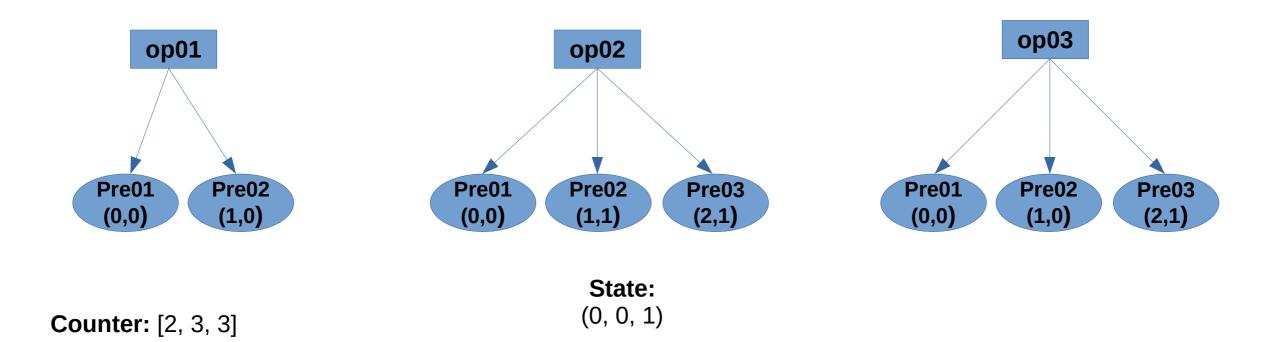
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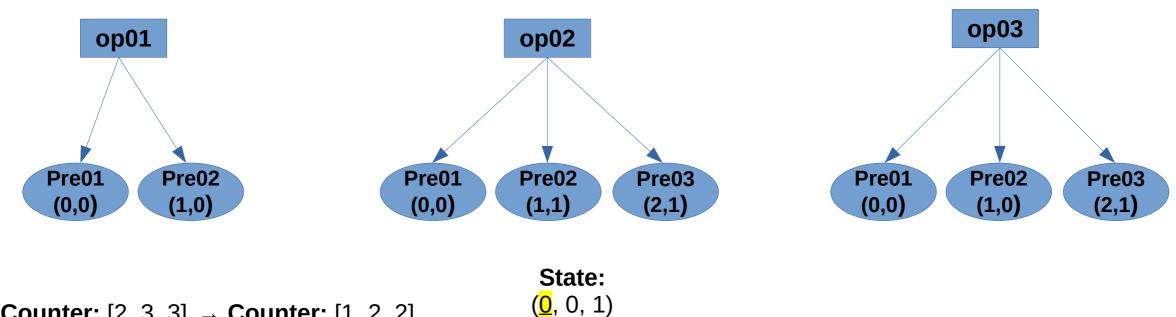




Precondition of:

| (0,0) | (0,1) | (1,0) | (1,1) | (2,0) | (2,1) |
|-------|-------|-------|-------|-------|-------|
| op01 | | op01 | op02 | | op02 |
| op02 | | op03 | | | op03 |
| op03 | | | | | |

Variable Domains: ({0,1}, {0,1}, {0,1})

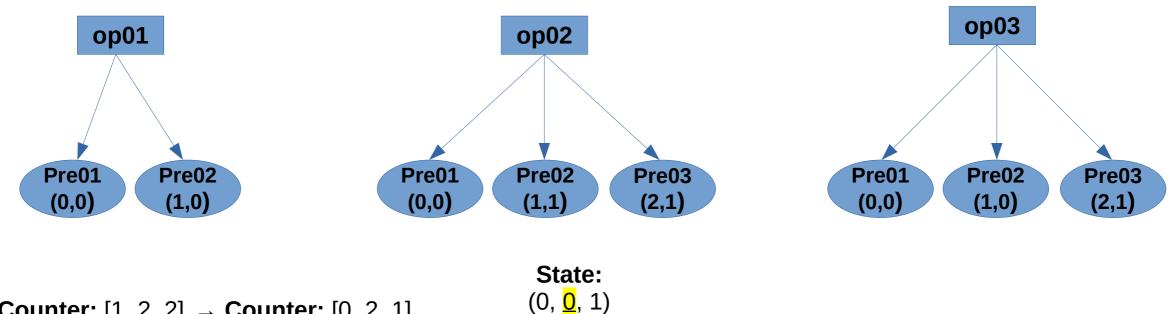


Counter: $[2, 3, 3] \rightarrow$ **Counter:** [1, 2, 2]

Precondition of:

| (0,0) | (0,1) | (1,0) | (1,1) | (2,0) | (2,1) |
|-------|-------|-------|-------|-------|-------|
| op01 | | op01 | op02 | | op02 |
| op02 | | op03 | | | op03 |
| op03 | | | | | |

Variable Domains: $(\{0,1\},\{0,1\},\{0,1\})$

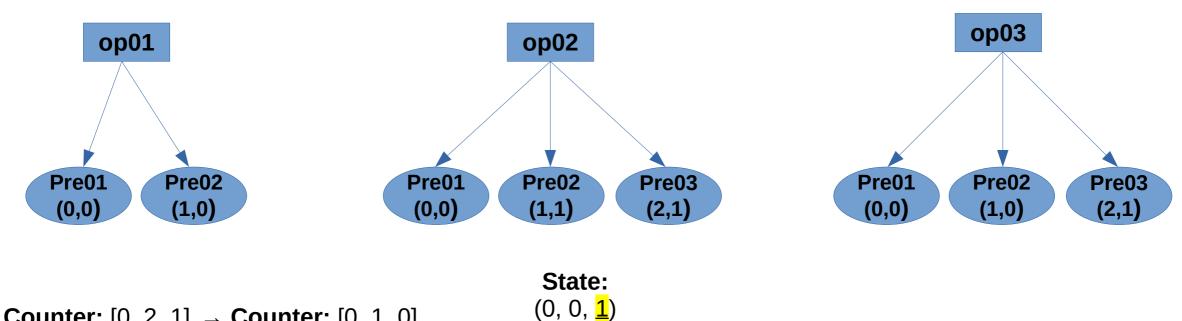


Counter: $[1, 2, 2] \rightarrow$ **Counter:** [0, 2, 1]

Precondition of:

| (0,0) | (0,1) | (1,0) | (1,1) | (2,0) | (2,1) |
|-------|-------|-------|-------|-------|-------|
| op01 | | op01 | op02 | | op02 |
| op02 | | op03 | | | op03 |
| op03 | | | | | |

Variable Domains: $(\{0,1\},\{0,1\},\{0,1\})$



Counter: $[0, 2, 1] \rightarrow$ **Counter:** [0, 1, 0]

Precondition of:

| (0,0) | (0,1) | (1,0) | (1,1) | (2,0) | (2,1) |
|-------|-------|-------|-------|-------|-------|
| op01 | | op01 | op02 | | op02 |
| op02 | | op02 | | | op03 |
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Vertex:

- Plausible operators
- Variable assignments
- Satisfied operators
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Idea:

- Choose variable v, which has not been assigned
- For each value in D_v a outgoing edge
- For each outgoing edge, new vertex, with values from parent
- Apply value to plausible operators and split (sat/unsat/plaus) and remove satisfied precons
- Remove variable assignments which aren't referenced anymore
- Check if vertex exists
 - If yes: edge goes to this one, stop recursion
 - If no: create new vertex and continue
- If DAG too big, restart and split operators in half

Vertex:

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Op01: {(0,0), (1,1)} Op02: {(0,0), (1,0)} Op03: {(0,1), (1,0)} Op04: {(0,1), (1,1)}

Variable Domains: $({0,1}, {0,1})$

Plaus: [1,2,3,4] Vars: [-1, -1] Sat: [] Choice: -1 Hash: ####

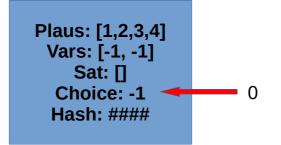
Op01: {(0,0), (1,1)}

Op02: {(0,0), (1,0)}

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Op04: {(0,1), (1,1)}

Variables Domains: ({0,1}, {0,1})



Op01: {(0,0), (1,1)}

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Op04: {(0,1), (1,1)}

Variable Domains: ({0,1}, {0,1})

Plaus: [1,2,3,4] Vars: [-1, -1] Sat: [] Choice: 0 Hash: ####

Op01: {(0,0), (1,1)}

Op02: {(0,0), (1,0)}

Op03: {(0,1), (1,0)}

Op04: {(0,1), (1,1)}

Variable Domains: ({0,1}, {0,1})

Plaus: [1,2,3,4] Vars: [-1, -1] Sat: [] Choice: 0 Hash: ####

Create Children

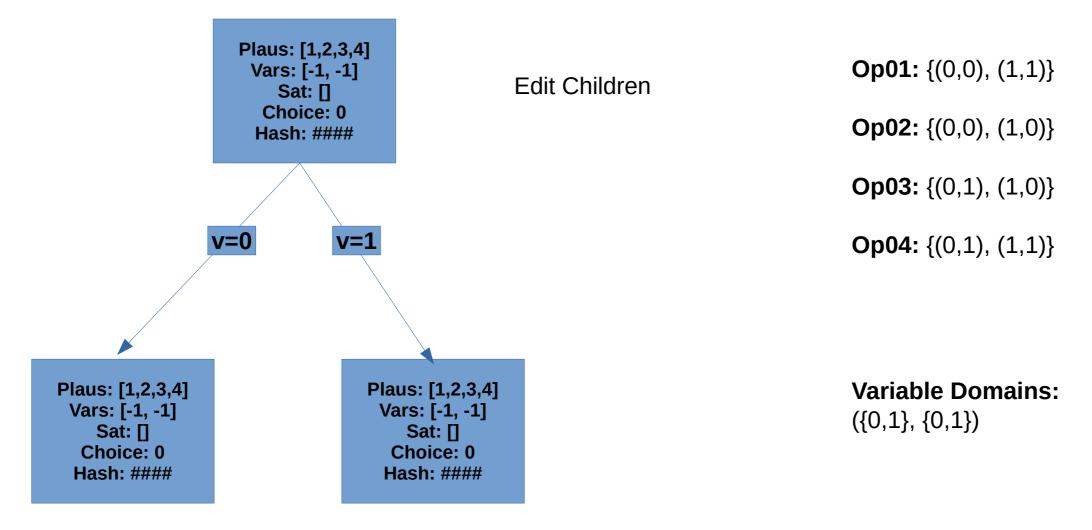
Op01: {(0,0), (1,1)}

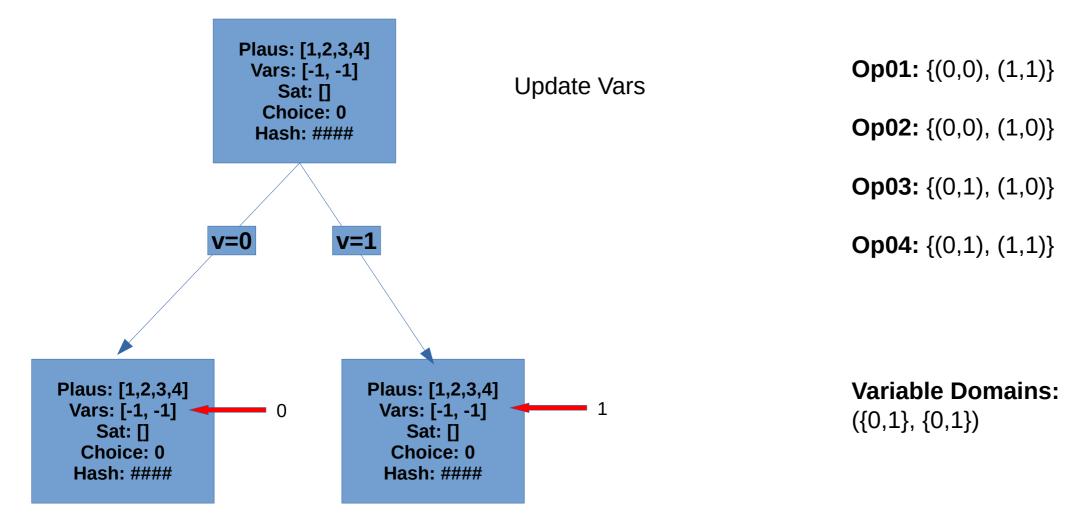
Op02: {(0,0), (1,0)}

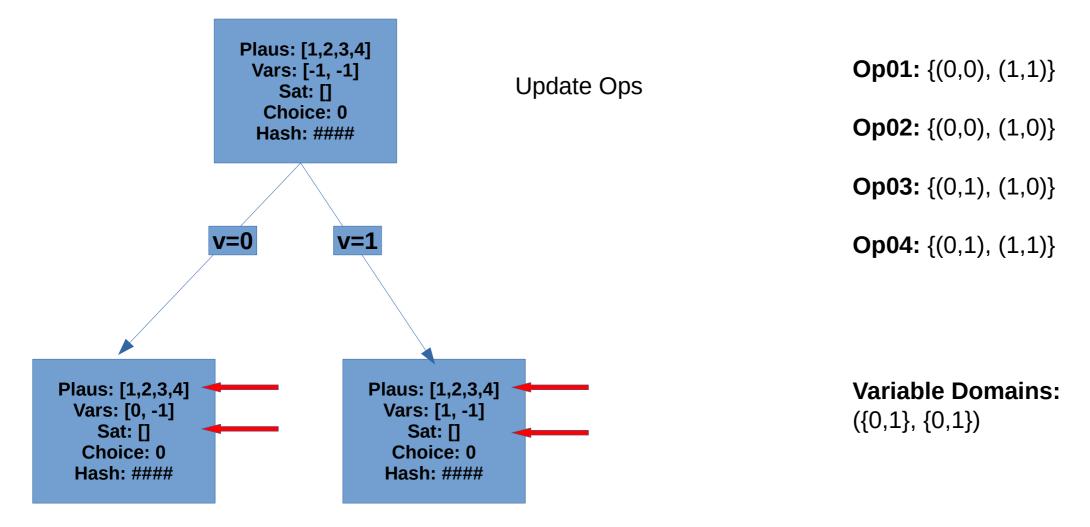
Op03: {(0,1), (1,0)}

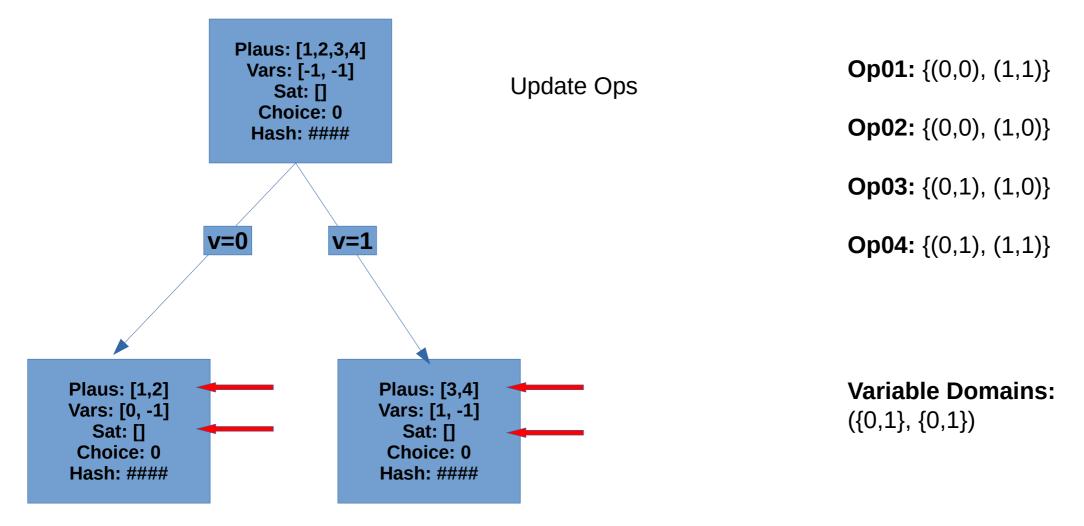
Op04: {(0,1), (1,1)}

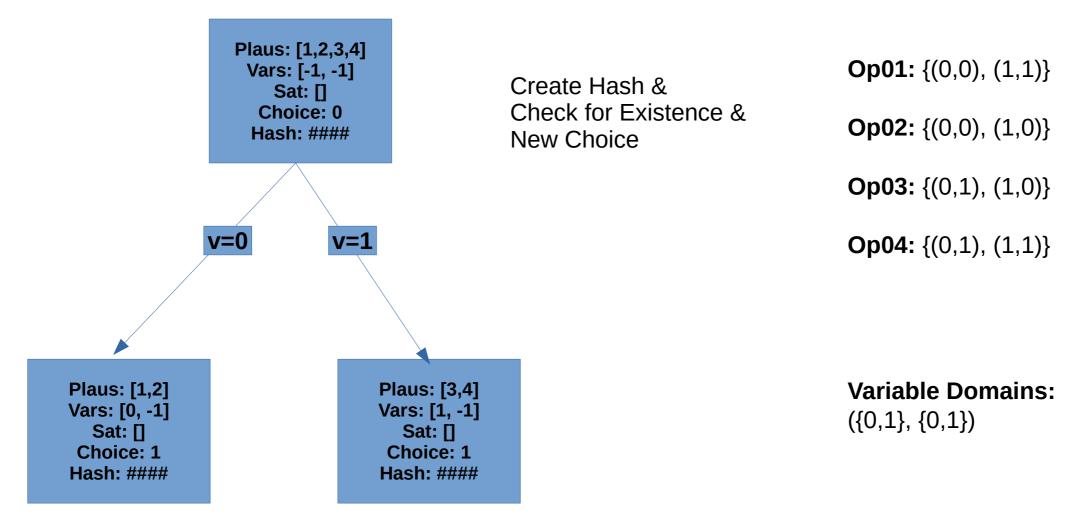
Variable Domains: ({0,1}, {0,1})

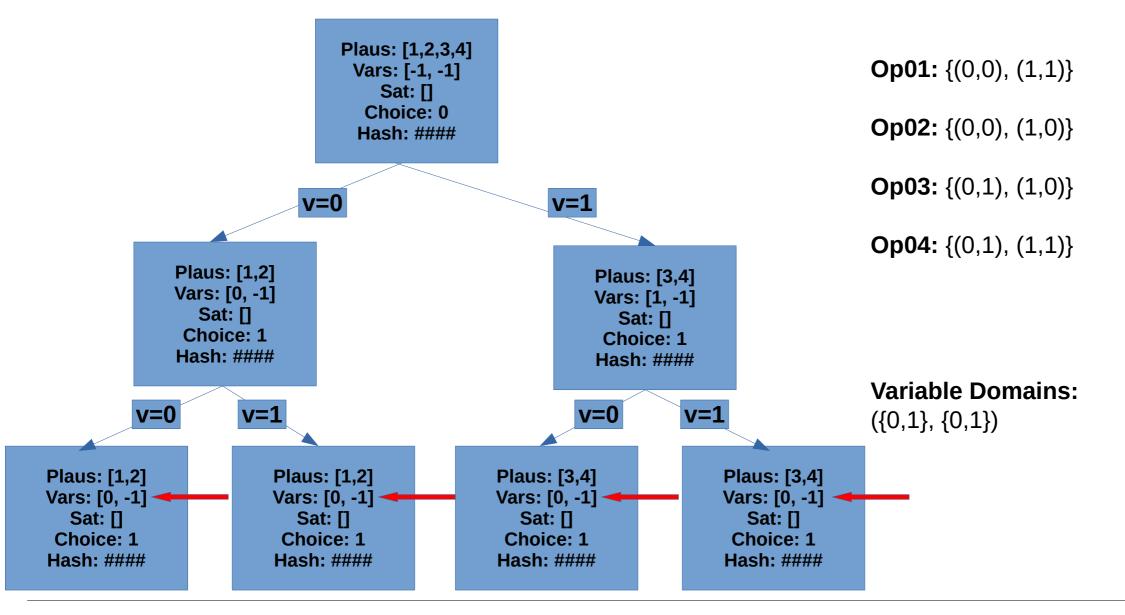


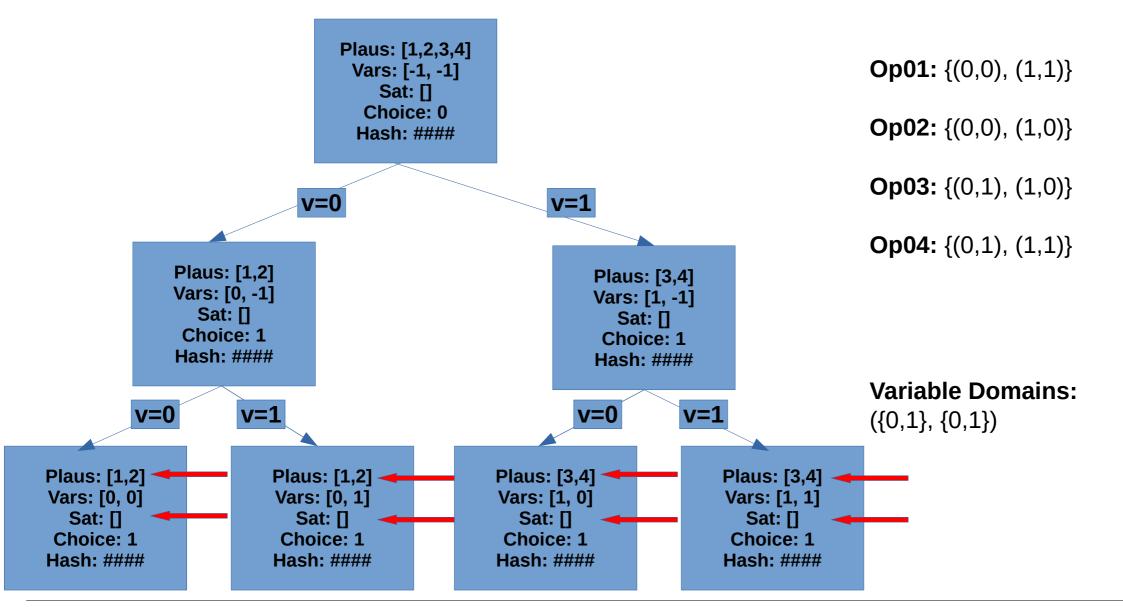


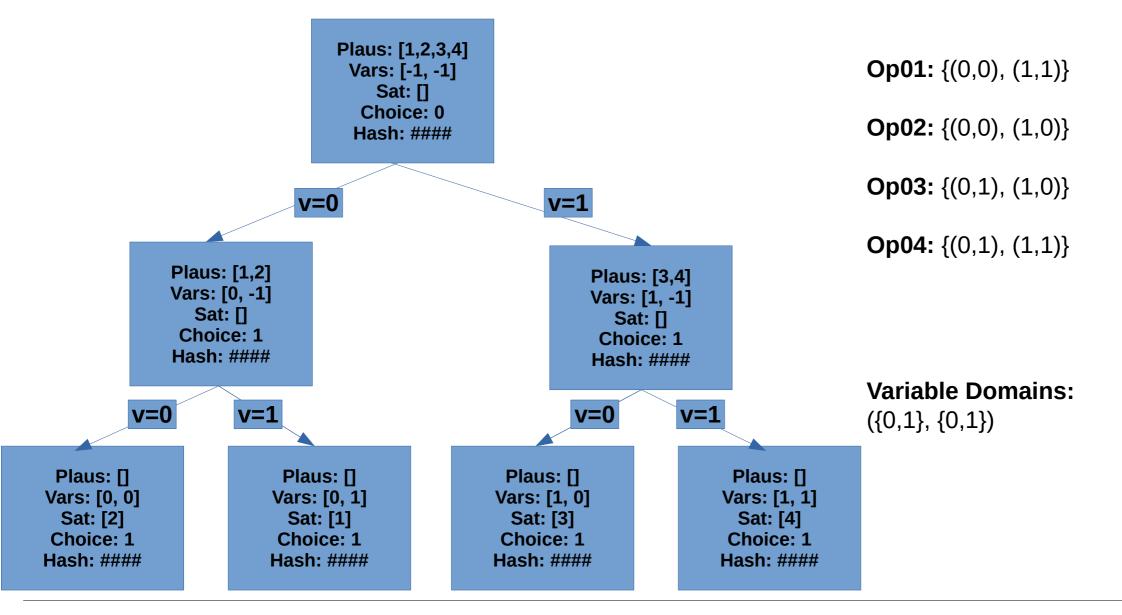




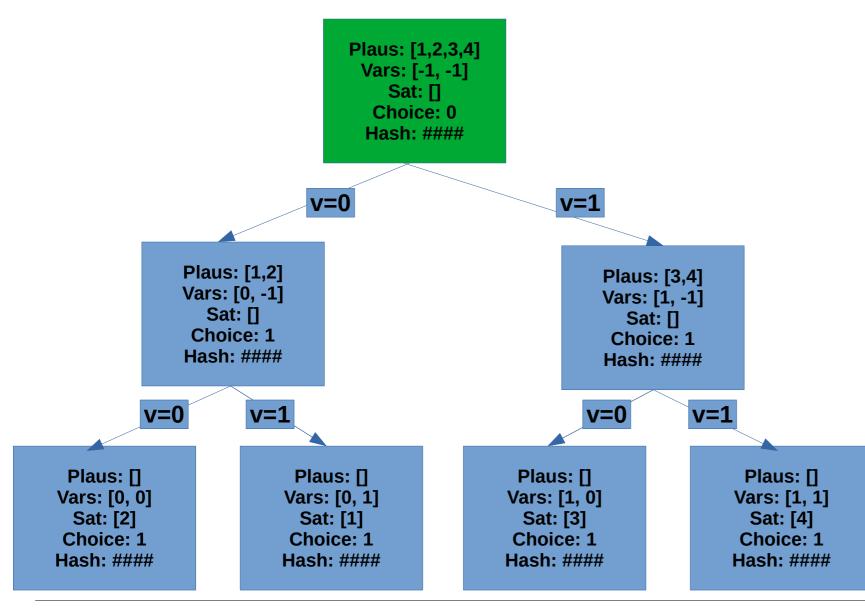




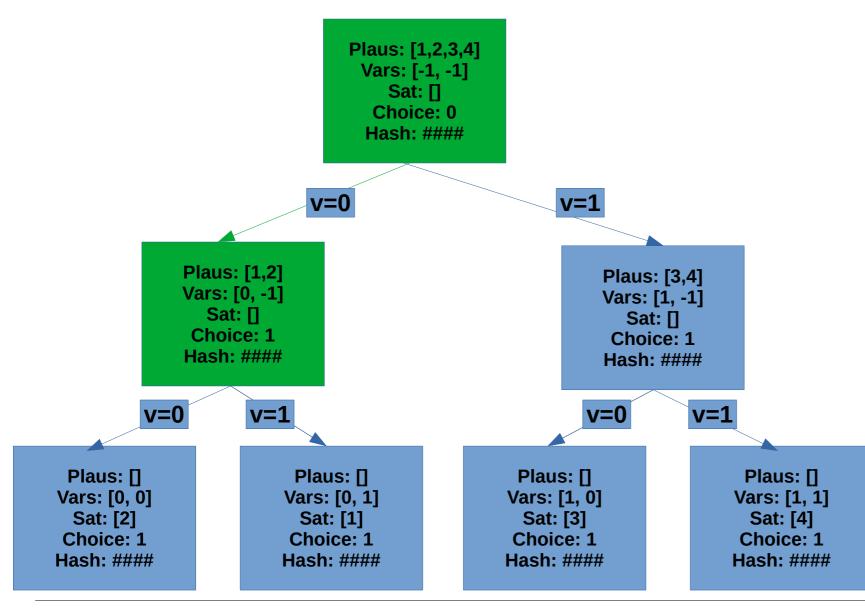




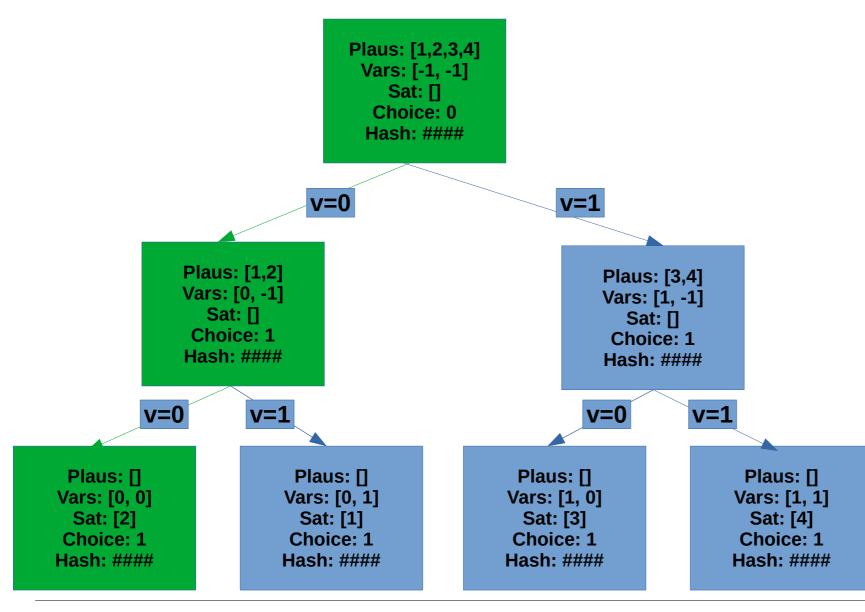
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SAT Solving:

• $(a \lor c) \land (\neg b \lor a) \land (\neg a \lor c \lor d)$

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- Check if satisfied
- If not, assign other value and check again
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- If only one literal left, we know how to assign that variable (unit propagation)
- If clause is satisfied, it stays that way

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Update to DPLL:

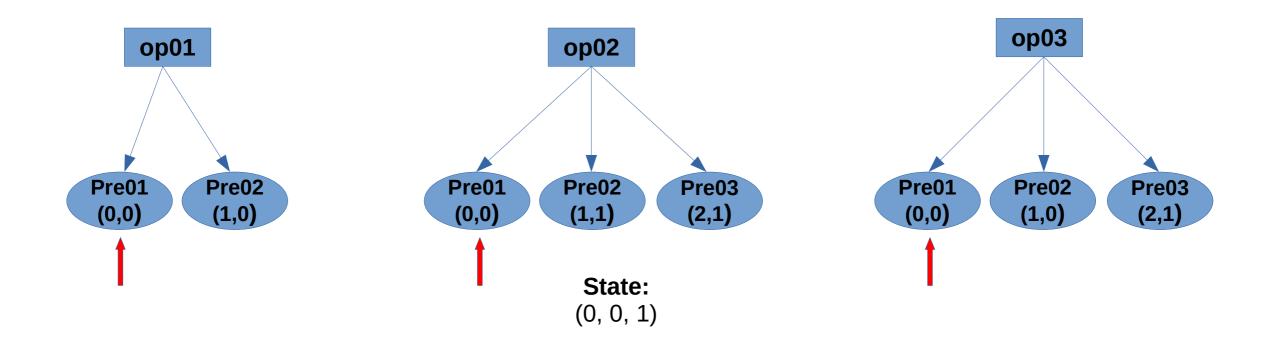
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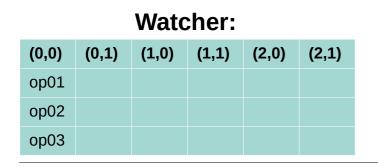
Improving DPLL → 2 Watched Literals:

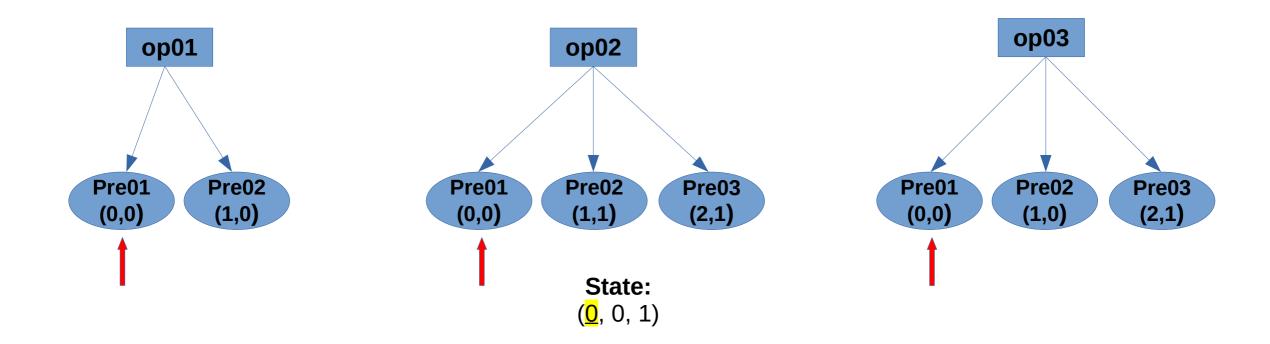
- Only want to know if one literal left for unit propagation
- Watch two literals:
 - If one satisfied, then clause is satisfied
 - If one unsatisfied, choose new unassigned to watch
 - If not possible \rightarrow unit propagation

Adaption:

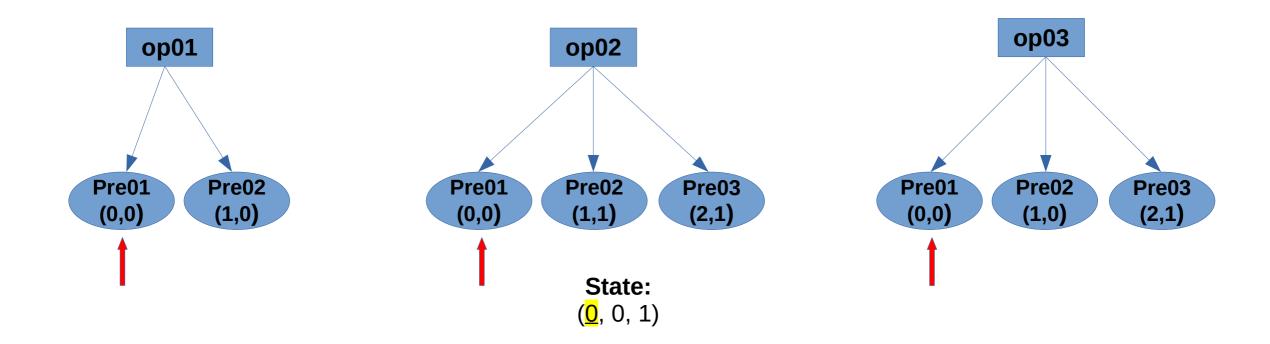
- All preconditions must be satisfied
- When checking state:
 - For each variable assignment in the state:
 - Check each operator watching that variable assignment
 - If any precondition unsatisfied, watch unsatisfied precondition
 - If all preconditions satisfied, operator is applicable

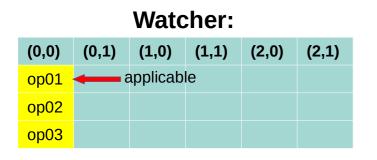


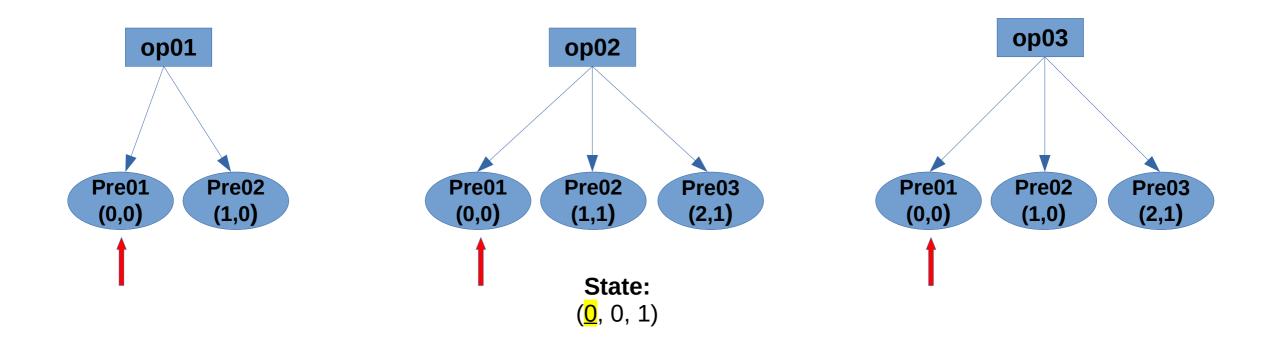


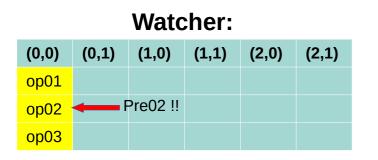


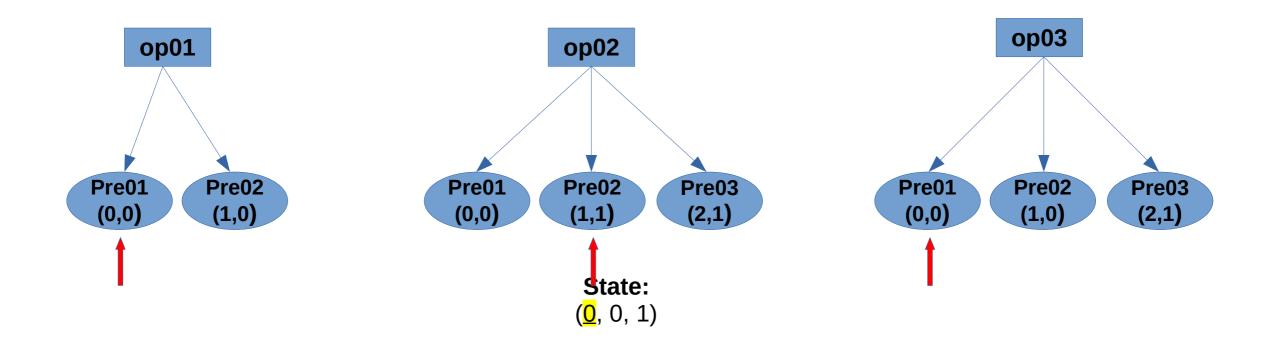




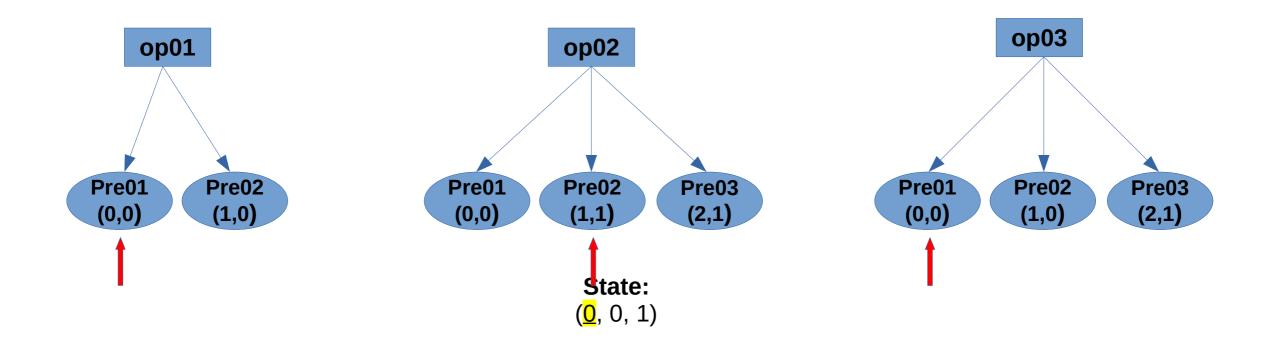


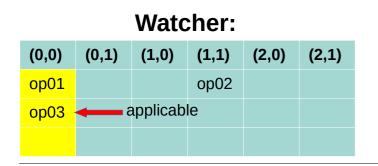


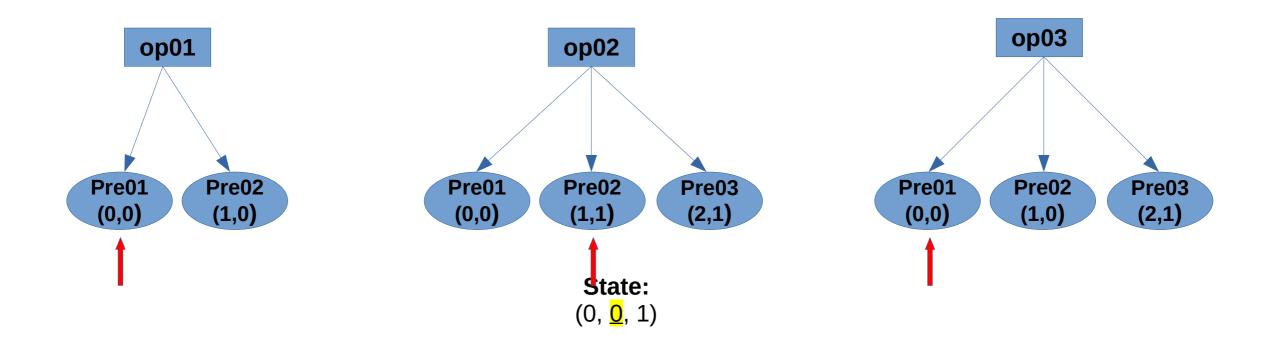




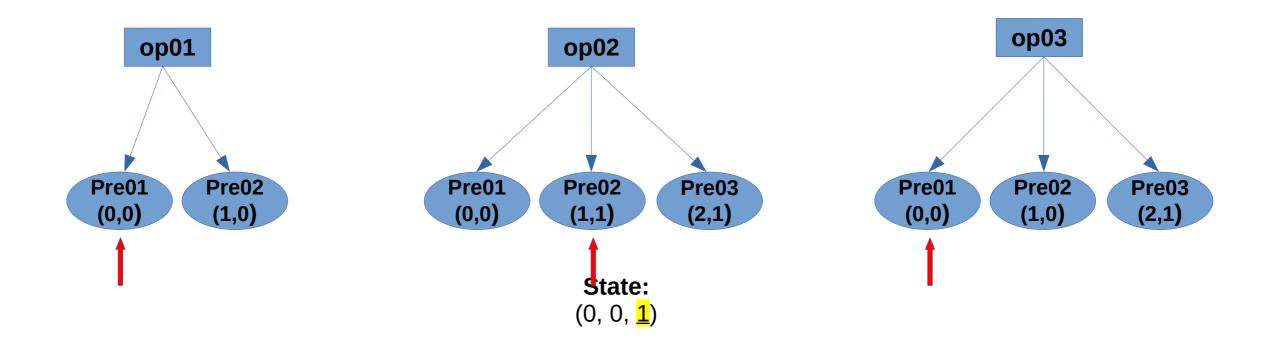














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7 Evaluation

Evaluation - How was tested

- A* with blind search
- 1827 different planning tasks from 65 different domains

Evaluation - Results

| Summary Unbound | Fast Downward | PSVN | Marking | Watched Literals | Naive |
|-------------------|---------------|--------|----------|---------------------|----------|
| Coverage | 712 | 253 | 680 | 658 | 689 |
| Out Of Memory | 1'098 | 1'522 | 1'006 | 866 | 773 |
| Out Of Time | 0 | 0 | 94 | 256 | 348 |
| SG Init Time | 0.08 | 335.54 | 0.59 | 0.02 | 0.01 |
| GAO Time | 841.53 | 873.75 | 1'592.82 | 3'079.83 | 3'735.03 |
| GAO Mean | 0.0014 | 0.0014 | 0.0026 | 0.0050 | 0.0061 |
| Total Time - Mean | 0.09 | 0.51 | 0.10 | 0.11 | 0.12 |

Evaluation - Conclusion

- No precomputation \rightarrow faster init time, less out of memory
- A lot precomputation \rightarrow faster GAO time, less out of time
- Trade off between faster initialization and faster GAO
- Choose correct successor generator for planning task!



Thank you for your attention.

Questions?